

CMPH - 307 Microprocessor and Embedded Systems

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Instructor: M.H. Assaf
Class Schedule: **Lecture** Monday, Wednesday 10:00 a.m. – 11:50 a.m.
Tutorial & Laboratory Tuesday 1:00 p.m. – 4:00 p.m.
Location: TBD
Lab time/location: TBD
Office Hours: 1:00 – 2:00 PM. Wednesday or by appointment
E-mail: mansour.assaf@utt.edu.tt
Course URL: www.u.tt/ict

Course Description

This is an undergraduate course on Microprocessor and Embedded Systems. Students in the class should have completed a course on computer organization and/or computer Architecture.

Prerequisite: CMON-207

Assessment:

Course Project (work in pairs) 25%
In-class Exams (2) 30%
Final Exam 45%
No late assignments will be accepted.

Reference Material:

F. Vahid and T. Givargis, Embedded System Design, Wiley 2002.
M. Rafiqzaman, Fundamentals of Digital Logic and Microcomputer Design, 5th Edition, Wiley 2005.

Laboratory/Course Projects: A list of suggested projects (TBD); Project presentation schedule (TBD);
Project report deadline (TBD)

Course Schedule

WEEK NO.	TOPICS AND READING	REFERENCES
1	Course Outline Introduction to microprocessor and embedded systems	F. Vahid and T. Givargis. (ch. 1) and Class notes.
2	Intel 8085 Microprocessor	Class notes.
3	Custom Single-Purpose processor	F. Vahid and T. Givargis. (ch. 2) and Class notes.
4	General-Purpose Processor	F. Vahid and T. Givargis. (ch. 3) and Class notes.
5	Intel 8086 Microprocessor	Class notes.
6	Processor Peripherals	F. Vahid and T. Givargis. (ch. 4) and Class notes.
7	Memory	F. Vahid and T. Givargis. (ch. 5) and Class notes.
8	Interfacing	F. Vahid and T. Givargis. (ch. 6) and Class notes.
9	Digital Camera Example	F. Vahid and T. Givargis. (ch. 7) and Class notes.
10	FINAL EXAM	Date TBD